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Method and a system for synchronizing clocks in a bus type local network
US Pat. 4815110 - Filed Mar 9, 1987 - U.S. Philips Corporation
multiplicity of counting pulses are suppressed in the **counter**. ... **message clock**
state no 10 correction of the **counter** takes place. ...

CP TRANSMITS INTERROGATE **MESSAGE** TO LOCK
US Pat. 3838395 - Filed Dec 4, 1972
F3 being zero, which indicates that the **message** being The **sync** code stored in
shift ... For example, a one bit in the going high clears **clock counter** C2, ...

Operations controller for a fault tolerant multiple node processing system
US Pat. 4980857 - Filed Jun 7, 1989 - Allied-Signal Inc.
The Synchronizer will then increment the **Sync Message Counter** 982, as indicated
by ... period count and a voted **sync clock** count using the extracted values. ...

Task scheduler for a fault tolerant multiple node processing system
US Pat. 4805107 - Filed Apr 15, 1987 - Allied-Signal Inc.
The Synchronizer will then increment the **Sync Message Counter** 982, as indicated
by ... period count and a voted **sync clock** count using the extracted values. ...

Operations controller for a fault tolerant multiple node processing system
US Pat. 4933940 - Filed May 12, 1989 - Allied-Signal Inc.
The Synchronizer will then increment the **Sync Message Counter** 982 ... voted
Subatomic period count and a voted **sync clock** count using the extracted values.
...

Communications protocol for digital telemetry system
US Pat. 5331318 - Filed Feb 26, 1993 - Schlumberger Technology Corporation
If the MSB of IP Departure **Clock** is zero and the MSB of IP **Message Clock** is one,
... the **Sync** word is missing. number of time bits that have to be sent up_ ...

Apparatus and method for synchronizing an SCDMA upstream or any other type ...
US Pat. 6353604 - Filed Mar 1, 2001 - Terayon Communications Systems, Inc.
The first **sync message** contains a sample of the timestamp **counter** 228 in the ...
is the number of CU timestamp **counter clock** ticks between time 256 in FIG. ...

TIME SLOT
US Pat. 3836726 - Filed Oct 25, 1971
29 30 The **sync** decoder 352 clocks the DATA signal The error **counter** 388 may be,
for example, ... 13, the **CLOCK** signal ZERO signal **synchronize** the CL32 and ...

Synchronizer for a fault tolerant multiple node processing system
US Pat. 4816989 - Filed Apr 15, 1987 - Allied-Signal Inc.
The synchronizer of claim 4 wherein said **sync** and stamp memory and said time ...
a time stamper connected to said **message** interface, 20 said **counter** means ...

Initialization and synchronization method for a two-way communication link
US Pat. 4910754 - Filed Sep 30, 1988 - Data General Corporation
The enable signal is provided to a four **clock counter** 40. ... The **message** in
assembly register 44 of FIG. 4 is passed into a **sync** latch 46. ...

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Simulcast transmission system having automtic synchronization
US Pat. 4696051 - Filed Dec 31, 1985 - Motorola Inc.
During its second mode, hereinafter referred to as the re-**sync** 5 mode, ...
counter 536 programmable N divider 540 and fixed **clock** 542 implement an ...

Communications protocol for digital telemetry system
US Pat. 5191326 - Filed Sep 5, 1991 - Schlumberger Technology Corporation
Every IP contains a **counter** (24 bits in the preferred embodiment) which ... If
the MSB of IP Departure **Clock** is zero and the MSB of IP **Message Clock** is one,
...

Anti-jam radio
US Pat. 4267592 - Filed Mar 30, 1979 - Rockwell International Corporation
The symbol **clock** and **message** timer are started from the **sync** pulse which is ...
and provides a X32 **clock** that is 32 times the **clock** provided by **counter** 303. ...

Well logging communication system
US Pat. 4355310 - Filed Aug 6, 1979 - Schlumberger Technology Corporation
This logic circuit 326 also delivers the "start of **message**" and "end of **message**"
signals ... When a frame **sync** pattern is detected a bit/word **counter** and a ...

Vital digital communication system
US Pat. 4090173 - Filed Dec 17, 1976 - General Signal Corporation
The Q output signal to the register 63 derived from the local **clock** of flip-flop
107 provides the clocking input to a **counter** and identified as **MESSAGE** ...

Method for maintaining a sequence of events function during failover in a ...
US Pat. 5426774 - Filed Apr 6, 1993 - Honeywell Inc.
After each IOL time **sync** broadcast, the pri- actually transmitted over the UCN,
the synchroniza- ... plus the **message** num- the current value of the **clock** X, ...

Selective paging receiver with **message** display
US Pat. 4816987 - Filed Dec 17, 1986 - NEC Corporation
A **sync** code detector 103 uses the read **clock** pulses from synchro- 30 nizing ...
3, the word **counter** 109 provides the BS timing signal to the BS timing ...

Data transmission system
US Pat. 4208650 - Filed Jan 30, 1978 - Forney Engineering Company
... to enable and adapted to increment the card address **counter** 142 by
synchronize the receiver 200 to receive the **message** one. frame. As the **sync** word
is ...

ECT GATE
US Pat. 3909792 - Filed Feb 26, 1973
This is equivalent to a 2- millisecond **clock** cycle, and it is each pulse at the
output of **counter** 105 which is used to derive the SWEEP **SYNC** pulse as will ...

Simulcast transmitter apparatus having automatic synchronization capability
US Pat. 4696052 - Filed Dec 31, 1985 - Motorola Inc.
... by said variable frequency **clock** to effect a delay in said **message** signal.

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Method of providing synchronous transport of packets between asynchronous ...
US Pat. 7000031 - Filed Apr 4, 2001 - Broadcom Corporation
Handset MAC devices use these timestamps to **synchronize** their local clocks ...
80, home gateway 2018 implements a **counter**/timer that is **sync**- locked to the ...

Automatic fuel dispensing control system
US Pat. 4263945 - Filed Jun 20, 1979
It also maintains the fueling complete timer in a reset state while correct **sync**
codes are received. The **message** length **counter** 78 counts the **message** 35 ...

SNAIAS digital data test set
US Pat. 4028536 - Filed Feb 27, 1976 - The United States of America as represented by the Secretary of the Navy
The sublabel update rate **counter** C44 counts the DATA SHIFT **CLOCK** signal. ...
sync A D-type flip flop T13 synchronizes the sample pe- burst, **message** loading
...

System and method for synchronization of video display outputs from multiple ...
US Pat. 6646645 - Filed Apr 23, 2002 - Quantum3D, Inc.
7 is a block diagram of a **sync** card 200 with an alternative interrupt scheme
utilizing a **clock counter** 240 for avoiding the buffer swap race hazard where ...

Method of enhancing network transmission on a priority-enabled frame-based ...
US Pat. 6954800 - Filed Apr 4, 2001 - Broadcom Corporation
An HPNA-POTS converter derives **clock** and grant timing information from received
Timestamp **Sync** and Timestamp Report **message** frames. ...

Superimposing method and apparatus useful for subliminal messages
US Pat. 5134484 - Filed Jun 1, 1989 - MindsEye Educational Systems, Inc.
92 (derived from the **message sync** signals) clears ad- FIGS. 8-1 and 8-2 show
another embodiment of the dress **counter** 126 and advances **counter** 127 by one ...

Signal encryption and distribution system for controlling scrambling and ...
US Pat. 4613901 - Filed May 27, 1983 - M/A-COM Linkabit, Inc.
The line sample **counter** controls circuitry in the control processor 202 that
provides a control signal to the one shot pulse generator in the **sync** detection
...

Apparatus for programmably accessing and assigning time slots in a time ...
US Pat. 5088089 - Filed Dec 15, 1989 - Alcatel NA Network Systems Corp.
In the event of failure, loss of **sync** or **clock**, the TSA is designed to switch
automatically ... An independent frame, word and bit **clock** and **counter** circuit
...

CO O UJ
US Pat. 3757032 - Filed Jan 28, 1971
At the end of a **message**, scribed hereinabove, wherein frame **sync** code is the ...
been received Latch 144 is AND 40 by **counter** 126 or column **clock counter** ...

Method of determining an end of a transmitted frame in a frame-based ...
US Pat. 6891881 - Filed Apr 4, 2001 - Broadcom Corporation